

UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s) : K. MIYANO et al.

**Certificate of Correction Branch**

Patent No. : 7,062,245

Issued: June 13, 2006

Serial No. : 09/739,817

Filed: December 20, 2000

For : RADIO TRANSMISSION APPARATUS AND RADIO  
RECEPTION APPARATUS

**COVER LETTER FOR REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

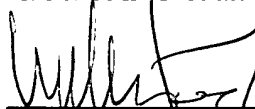
**Certificate**  
**SEP 26 2006**  
**of Correction**

Sir:

Attached are a Request and a proposed Certificate of Correction in connection with the above-identified patent.

A check in the amount of \$100.00 is enclosed to cover the fee. The Commissioner is hereby authorized to charge any additional fees, or credit any overpayment, to Deposit Account No. 19-0089.

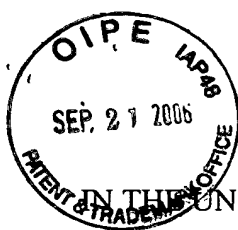
Respectfully submitted,  
K. MIYANO et al.

  
\_\_\_\_\_  
Bruce H. Bernstein  
Reg. No. 29,027

**William Pieprz**  
**Reg. No. 33630**

September 21, 2006  
GREENBLUM & BERNSTEIN, P.L.C.  
1950 Roland Clarke Place  
Reston, VA 20191  
(703) 716-1191

**SEP 27 2006**



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Sir:

Please find attached a proposed Certificate of Correction.

Please correct the following errors appearing in the printed patent which are apparently the fault of the U.S. Patent and Trademark Office, as per the attached Certificate of Correction.

At column 1, line 37 of the printed patent, "Is" should be ---is---. This appeared correctly on page 2, line 11 of the Specification filed December 20, 2000.

At column 1, line 37 of the printed patent, "'0 or 0'" should be ---"1 or 0"---. This appeared correctly on page 2, line 12 of the Specification filed December 20, 2000.

At column 5, line 23 of the printed patent, "Is" should be ---is---. This appeared correctly on page 12, line 12 of the Specification filed December 20, 2000.

The following errors are apparently the fault of the Inventor(s):

At column 3, line 16 of the printed patent, "comparing circuit 117" should be ---comparing circuit 116---. This appeared incorrectly on page 6, line 19 of the Specification filed December 20, 2000.

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At column 3, line 54 of the printed patent, "polarization antenna 106" should be ---polarization antenna 107---. This appeared incorrectly on page 8, line 8 of the Specification filed December 20, 2000.

At column 6, line 16 of the printed patent, "multiplier 303" should be ---multiplier 301---. This appeared incorrectly on page 14, line 22 of the Specification filed December 20, 2000.

At column 6, line 17 of the printed patent, "transmission signal 303" should be ---transmission signal 302---. This appeared incorrectly on page 14, line 24 of the Specification filed December 20, 2000.

At column 6, line 20 of the printed patent, "transmission signal 303" should be ---transmission signal 302---. This appeared incorrectly on page 14, line 26 of the Specification filed December 20, 2000.

At column 6, line 33 of the printed patent, "D-flip flop" should be ---D-flip flop 401---. This appeared incorrectly on page 15, line 15 of the Specification filed December 20, 2000.

Therefore, it is respectfully requested that a Certificate of Correction issue in the above-identified patent as follows:

At column 1, line 37 of the printed patent, "Is" should be ---is---.

At column 1, line 37 of the printed patent, "'0 or 0'" should be ---"1 or 0"---.

At column 5, line 23 of the printed patent, "Is" should be ---is---.

At column 3, line 16 of the printed patent, "comparing circuit 117" should be ---comparing circuit 116---.

At column 3, line 54 of the printed patent, "polarization antenna 106" should be ---polarization antenna 107---.

At column 6, line 16 of the printed patent, "multiplier 303" should be ---multiplier 301---.

At column 6, line 17 of the printed patent, "transmission signal 303" should be ---transmission signal 302---.

At column 6, line 20 of the printed patent, "transmission signal 303" should be ---transmission signal 302---.

At column 6, line 33 of the printed patent, "D-flip flop" should be ---D-flip flop 401---.

Should there be any questions, the Examiner is invited to contact the undersigned at the below-listed number.

Respectfully submitted,  
K. MIYANO et al.



Bruce H. Bernstein  
Reg. No. 29,027

William Pieprz  
Reg. No. 33630

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Page 1 of 1

PATENT NO. : 7,062,245  
APPLICATION NO.: 09/739,817  
ISSUE DATE : June 13, 2006  
INVENTOR(S) : K. MIYANO et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- At column 1, line 37 of the printed patent, "Is" should be ---is---
- At column 1, line 37 of the printed patent, "0 or 0" should be ---"1 or 0"---
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- At column 3, line 16 of the printed patent, "comparing circuit 117" should be ---comparing circuit 116---
- At column 3, line 54 of the printed patent, "polarization antenna 106" should be ---polarization antenna 107---
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- At column 6, line 20 of the printed patent, "transmission signal 303" should be ---transmission signal 302---
- At column 6, line 33 of the printed patent, "D-flip flop" should be ---D-flip flop 401---

**MAILING ADDRESS OF SENDER (Please do not use customer number below):**

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SEP 27 2006

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